



501.40678CX1

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): J. NOGUCHI, et al  
Application No.: 10/742,932  
Filed: December 23, 2003  
For: FABRICATION METHOD OF SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE  
Group: TBD  
Examiner: TBD

**SUPPLEMENTAL PRELIMINARY AMENDMENT**

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

February 2, 2004

Sir:

Supplementing the Preliminary Amendment filed December 23, 2003, please amend the above-identified application as listed in the following, and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the amendments.